

## ABSTRACT

An objective of the present invention is to provide a data transfer control device and electronic equipment which make it  
5 possible to reduce processing overheads in the firmware and implement high-speed data transfer. In a data transfer control device in accordance with the IEEE 1394 standard, the header of a packet is written to a header area, the ORB (data for SBP-2) of the packet is written to an ORB area, and the stream (data  
10 for the application layer) of the packet is written to a stream area. The stream area is managed by hardware in accordance with full and empty signals. Indication information is comprised within a transaction label  $t_1$  of a request packet, and the header, ORB, and stream of a response packet are written to areas  
15 indicated by the indication information comprised within  $t_1$ , when the response packet is received. The device is also provided with registers TSR and TER that contain addresses TS and TE for securing a transmission area in the stream area and registers RSR and RER that contain addresses RS and RE for  
20 securing a reception area therein.